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Application ID: 10643208 
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High Precision Digital Delay
Lines Therein
First Named Inventor: Robert Proebsting
Domestic/Foreign Application: Domestic Application
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Confirmation number: 4050
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**TRANSMITTAL**

Electronic Version v1.1
Stylesheet Version v1.1.0


Title of Invention	Integrated Circuit Devices Having High Precision Digital Delay Lines Therein									
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Grant J. Scott Registered Number: 36925</td><td>/gjs/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Grant J. Scott Registered Number: 36925	/gjs/	Attorney		
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>5646-81-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	5646-81-usidst.xml		us-ids.dtd		us-ids.xsl
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us-ids	5646-81-usidst.xml									
	us-ids.dtd									
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Comments										



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Electronic Version v18

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